

CLAIM LISTING

1. (Currently Amended) A transport stream feeder for verifying a video decoder, said transport stream feeder comprising:

a digital input/output card, said digital input/output card comprising:

a first memory for storing a reference video;

a processor for encoding the reference video, providing the encoded reference video to the video decoder, and receiving a decoded reference video from the video decoder and writing the decoded reference video to the second memory;
and

~~a the second memory for storing a the decoded reference video, the decoded reference video being decoded by the video decoder.~~

2. (Original) The transport stream feeder of claim 1, wherein the digital input/output card further comprises:

an interface for transmitting the reference video to the video decoder.

3. (Original) The transport stream feeder of claim 1, further comprising:

an adapter for connecting the digital input/output card to the video decoder.

4. (Original) The transport stream feeder of claim 1, wherein the digital input/output card further comprises:

a third memory for storing a plurality of instructions executable by the processor, wherein execution of the plurality of instructions by the processor causes:

encoding the reference video;

transmitting the encoded reference video;

receiving the decoded reference video from the video

decoder; and

storing the decoded reference video from the video decoder in the second memory.

5. (Original) The transport stream feeder of claim 1, wherein the encoded reference video comprises an MPEG transport stream.

6. (Currently Amended) A transport stream feeder for verifying a video decoder, said transport stream feeder comprising:

a digital input/output card, said digital input/output card

comprising:

a first memory for storing a reference video;

a processor connected to the first memory, the processor for encoding the reference video, providing the encoded reference video to the video decoder, and receiving a decoded reference video from the video decoder, and writing the decoded reference video to a second memory; and

a ~~the~~ second memory connected to the processor, the second memory for storing a the decoded reference video ~~the decoded reference video being decoded by the video decoder.~~

7. (Original) The transport stream feeder of claim 6, wherein the digital input/output card further comprises:

an interface operatively coupled to the processor, the interface for transmitting the reference video to the video decoder.

8. (Original) The transport stream feeder of claim 6, further comprising:

an adapter operatively coupled to the interface, the adapter for coupling the digital input/output card to the video decoder.

9. (Original) The transport stream feeder of claim 6, wherein the digital input/output card further comprises:

a third memory operatively coupled to the processor, the third memory for storing a plurality of instructions executable by the processor, wherein execution of the plurality of instructions by the processor causes:

encoding the reference video;

transmitting the encoded reference video;

receiving the decoded reference video from the video

decoder; and

storing the decoded reference video from the video decoder

in the second memory.

10. (Original) The transport stream feeder of claim 6, wherein the encoded reference video comprises an MPEG transport stream.